L Number	Hits	Search Text	DB	Time stamp
1	231	(memory adj device) same (internal with memory with bus) same (external with memory) same control$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/30 13:32
2	110	365/$.ccls. and (memory adj device) same (internal with memory with bus) same (external with memory) same control$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/30 13:33
3	48	365/230.03,238.5,63,51,201,226-229.ccls. and (memory adj device) same (internal with memory with bus) same (external with memory) same control$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/30 13:35
4	40	365/230.03,238.5,63,51,201,145.ccls. and (memory adj device) same (internal with memory with bus) same (external with memory) same control$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/30 13:44
5	1	365/230.03,238.5,63,51,201,145.ccls. and (memory adj device) same (internal with memory with bus) same (external with memory) same control$4 same (page with mode)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/30 13:46
7	38	365/230.03,238.5,63,51,201,145.ccls. and (memory adj device) same (internal with memory with bus) same (external with memory with bus) same control$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/30 13:50
9	1	"5912849".PN.	USPAT	2004/09/30 13:49
12	1	365/230.03,238.5,63,51,201,145.ccls. and (memory adj device) same (internal with bus) same (external with bus) same control$4 same (page with mode)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/30 13:52
13	13	365/230.03,238.5,63,51,201,145.ccls. and (memory adj device) same (internal with bus) same (external with bus) same control$4 same mode	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/30 13:53
14	1	"5912849".PN.	USPAT	2004/09/30 13:53
15	5	365/230.03,238.5,63,51,201,145.ccls. and (memory adj device) same (internal with bus) same (external with bus) same $5control$4 same test$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/30 13:55
16	5	365/230.03,238.5,63,51,201,145.ccls. and (memory adj device) same (internal with control$4) same (external with control$4) same bus same test$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/30 13:56
17	47	365/230.03,238.5,63,51,201,145.ccls. and (memory adj device) same (internal with control$4) same (external with control$4) same bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/30 14:01
18	2	6,629,185.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/30 14:01
-	9	asynchronous same synchronous same (page adj mode) same (burst adj mode)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/27 13:09

-	56	asynchronous and synchronous same (page adj mode) and (burst adj mode)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/27 13:09
-	31	365/$.ccls. and asynchronous and synchronous same (page adj mode) and (burst adj mode)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/27 13:36
-	42	365/$.ccls. and asynchronous same synchronous same page same burst	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/27 13:50
-	1	365/$.ccls. and asynchronous same synchronous same page same burst same (memory adj bus) same (system adj bus) same (memory adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/27 13:52
-	1	asynchronous same synchronous same page same burst same (memory adj bus) same (system adj bus) same (memory adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/27 13:55
-	9	365/$.ccls. and asynchronous and synchronous and page and burst and (memory adj bus) and (system adj bus) and (memory adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 09:37
-	11	("4850891" | "5721860" | "5790447" | "5982655" | "6067593" | "6078515" | "6111757" | "6173382" | "6285558" | "6298426" | "6353539").PN.	USPAT	2004/09/27 14:14
-	8	("5379304" | "5390308" | "5412788" | "5450422" | "5745914" | "5765188" | "5860134" | "5897663").PN.	USPAT	2004/09/27 14:18
-	34	365/$.ccls. and page and burst and (memory adj bus) and (system adj bus) and (memory adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/27 14:31
-	1	365/$.ccls. and page and burst and FeRAM and (memory adj bus) and (system adj bus) and (memory adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/27 14:31
-	4	365/$.ccls. and page and burst and FeRAM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/27 14:34
-	310	365/$.ccls. and FeRAM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/27 14:34
-	1	365/$.ccls. and (memory adj bus) and FeRAM and (system adj bus) and (memory adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/27 14:35
-	1	(memory adj bus) and FeRAM and (system adj bus) and (memory adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/27 14:35
-	4645	(memory adj bus) and (system adj bus) and (memory adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/27 14:36

–	6	365/$.ccls. and SRAM and SDRAM and (memory adj bus) and (system adj bus) and (memory adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/27 14:37
–	1	((asynchronous and (synchronous with page) and (synchronous with burst)) with (memory adj controller)) and (memory adj bus) and (system adj bus) and (memory adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 09:42
–	1	((asynchronous and (synchronous with page) and (synchronous with burst)) with (memory adj controller)) and (memory adj bus) and (system adj bus)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 09:43
–	1	((asynchronous and (synchronous with page) and (synchronous with burst)) with (memory adj controller))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 09:42
–	3	((asynchronous and page and burst) with (memory adj controller)) and (memory adj bus) and (system adj bus)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 09:51
–	6	((SRAM same SDRAM same DRAM) with (memory adj controller$1)) and (memory adj bus) and (system adj bus)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 09:56
–	94	((SRAM same SDRAM same DRAM) with (memory adj controller$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 09:56
–	8	365/$.ccls. and ((SRAM same SDRAM same DRAM) with (memory adj controller$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 10:07
–	3	6,215692.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 10:07
–	96	asynchronous and synchronous and page and burst and (memory adj bus) and (system adj bus) and (memory adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 13:29
–	11	("4850891" | "5721860" | "5790447" | "5982655" | "6067593" | "6078515" | "6111757" | "6173382" | "6285558" | "6298426" | "6353539").PN.	USPAT	2004/09/28 11:56
–	3	("5604883" | "5649161" | "5664153").PN.	USPAT	2004/09/28 12:13
–	427	(memory adj bus) and synchronous adj3 (FeRAM or memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 13:30
–	89	((memory adj bus) and synchronous adj3 (FeRAM or memory)) and (page adj mode)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 13:31
–	82	(((memory adj bus) and synchronous adj3 (FeRAM or memory)) and (page adj mode)) and (controller or (control adj unit) or (control adj circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 13:32

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–	72	((((memory adj bus) and synchronous adj3 (FeRAM or memory)) and (page adj mode)) and (controller or (control adj unit) or (control adj circuit))) and external	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 13:32
–	17	((((memory adj bus) and synchronous adj3 (FeRAM or memory)) and (page adj mode)) and (controller or (control adj unit) or (control adj circuit))) and (external adj2 bus)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 13:36
–	7	(((((memory adj bus) and synchronous adj3 (FeRAM or memory)) and (page adj mode)) and (controller or (control adj unit) or (control adj circuit))) and (external adj2 bus)) and (burst adj mode)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 13:38
–	362	((memory adj bus) and synchronous adj3 (FeRAM or memory)) and controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 13:39
–	8927	memory and (external adj2 bus)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 13:39
–	85	(((memory adj bus) and synchronous adj3 (FeRAM or memory)) and controller) and (memory and (external adj2 bus))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 13:40
–	66	((((memory adj bus) and synchronous adj3 (FeRAM or memory)) and controller) and (memory and (external adj2 bus))) and (transfer adj2 data)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 13:41
–	27	((((memory adj bus) and synchronous adj3 (FeRAM or memory)) and controller) and (memory and (external adj2 bus))) and (transfer adj2 data) and (external adj2 control$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/28 13:42
–	2	6,526451.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/30 07:35
–	2	6,125,061.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/30 13:20
–	2	6,266,285.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/30 13:21
–	1	6,426,916.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/30 13:21
–	1	6,415,339.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/30 13:21
–	2	6,314,251.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/30 13:22
–	1	"6034891".PN.	USPAT	2004/09/30 13:22

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